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Re: U.S. Patent Application Serial No. 10/724,984 filed November 30, 2003  
Entitled: Method For Optimizing MRAM Circuit Performance

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### Message:

Dear Examiner Hoang:

Enclosed for filing in connection with the above-referenced application are the following:

- \* Request for Continued Examination Transmittal (1 pg.) [1 original, 1 copy]
- \* Fee Transmittal (1 pg.) [1 original, 1 copy]
- \* Amendment (4 pp.)
- \* Information Disclosure Statement with Form PTO/SB/08A (2 pp.)

Respectfully submitted,

Paul E. Franz

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